



Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
USPTO Use Only

EFS ID: 50031  
Application ID: 10623303  
Title of Invention: Half-Rate Clock Logic Block and  
Method for Forming Same  
First Named Inventor: Jeffrey Grundvig  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2003-07-18  
Effective Receipt Date: 2003-10-31  
Submission Type: Information Disclosure  
Statement  
Filing Type:  
Confirmation number: 3436  
Attorney Docket Number: 075903-197




Total Fees Authorized:

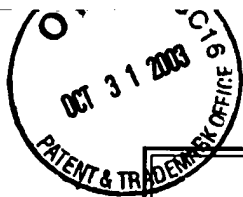
Digital Certificate Holder: cn=John L. DeAngelis,ou=Registered Attorneys,ou=Patent and  
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US  
Certificate Message Digest: 3d1879f98e815fed378b0873bbbf0b48fbd03dfe



# TRANSMITTAL

Electronic Version v1.1  
Stylesheet Version v1.1.0


Title of Invention	Half-Rate Clock Logic Block and Method for Forming Same									
Application Number: 10/623303 										
Date: 2003-07-18										
First Named Applicant: Jeffrey Paul Grundvig										
Confirmation Number: 3436										
Attorney Docket Number: 075903-197										
<p>I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.</p> <p>I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.</p>										
<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>John L. DeAngelis Jr. Registered Number: 30,622</td><td>/John L. DeAngelis, Jr./</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	John L. DeAngelis Jr. Registered Number: 30,622	/John L. DeAngelis, Jr./	Attorney		
Submitted by:	Elec. Sign.	Sign. Capacity								
John L. DeAngelis Jr. Registered Number: 30,622	/John L. DeAngelis, Jr./	Attorney								
<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>Agere197-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	Agere197-usidst.xml		us-ids.dtd		us-ids.xsl
Documents being submitted	Files									
us-ids	Agere197-usidst.xml									
	us-ids.dtd									
	us-ids.xsl									
Comments										



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	<b>Half-Rate Clock Logic Block and Method for Forming Same</b>																				
<p>Application Number: 10/623303 </p> <p>Confirmation Number: 3436</p> <p>First Named Applicant: Jeffrey Grundvig</p> <p>Attorney Docket Number: 075903-197</p> <p>Art Unit: 2816</p> <p>Search string: ( 5301196 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>5301196</td><td>1994-04-05</td><td>Ewen, et al</td><td></td><td></td><td></td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	5301196	1994-04-05	Ewen, et al				Examiner Name	Date		
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass														
	1	5301196	1994-04-05	Ewen, et al																	
Examiner Name	Date																				